

Standard Specification Sheet Model: MS90-SY Plug-in High Performance Arrester for Synchro Device (90V AC)

1 when ordering)

ARRESTANT

OVERVIEW

ARRESTANT is a series of high performance lightning arresters that protect electronic instruments by absorbing surge voltage induced by lightning. To protect the instruments effectively, arresters shall be installed adjacent to instrument to be protected against lines where surge voltage is induced.

MS90-SY is designed to protect signal lines between synchro devices.

ORDERING INFORMATION

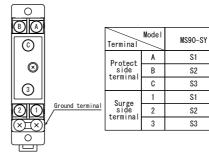
Ordering Code	Standard Price
N000 0V	OPEN
MS90-SY	

SPECIFICATIONS		
INPUT AND OUTPUT SECTION		
Input Signal	90V AC	
Output	90V AC	
Signal		
PERFORMANCE		
Allowable	Between lines: 1mA or less (at 200V DC)	
Current	Between ground: 1mA or less	
Leakage	(at 400V DC)	
Instrument	Between lines: 500V or less	
Side Voltage	(at 10kV, $1.2/50 \mu s$)	
Limit	Between ground: 1000V or less	
	(at 10kV, $1.2/50 \mu s$)	
Surge	Between lines: 1000A (at $8/20 \mu$ s)	
Discharge	Between ground: 5000A (at $8/20 \mu$ s)	
Withstand		
Capacity		
Discharge	Between lines: 240V or more	
Starting	Between ground: 420V or more	
Maximum	100mA	
Load		
Current		
Temperature	_10~60°C	
Humidity	5~90%RH (no condensation)	
PHYSICAL	5 - 90 MATE (110 Condensation)	
Construction	2 sections construction of main body and	
	terminal base:	
	plug-in + screw fastening	
Material	Black, flame resistance ABS resin (Case body)	
	Black, flame resistance ABS resin	
	(Terminal base)	
Grounding	JIS Class 3 grounding [100Ω or less]	
	(with grounding bar)	
Mounting	337 11	
Method	Wall mounting with M4 screw	
Connecting	M4 screw terminal connection	
Method	(fastening torque 1.2 N·m or less)	

External	W23.5×H100×D55mm	
Dimension		
Mounting	00000	
Dimension	90mm	
Multi-mounting	9.4	
Pitch	24mm	
Weight	Approx. 110g	
ADDITIONAL		
Adaptor for	■ With an adaptor for DIN rail mounting	
DIN rail	/D ■ Without an adaptor for DIN rail mounting	
mounting	Without all adaptor for DIN rail inoditing	
(Specify at		



TERMINAL ASSIGNMENT



BLOCK DIAGRAM

