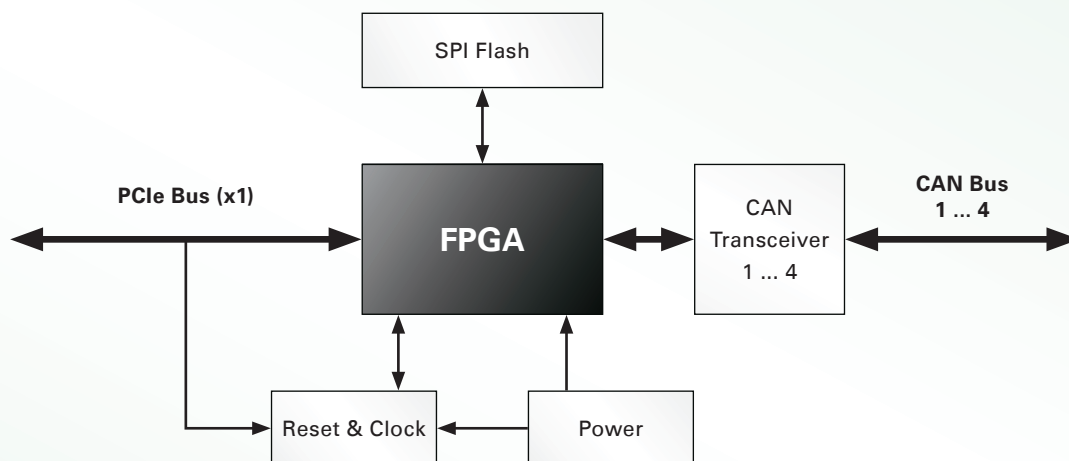


PCAN-Chip PCIe FD

Chip Solutions for the CAN FD Connection to PCI Express



For the CAN and CAN FD connection of your hardware design we provide several solutions.

Based on an FPGA, up to 4 CAN channels can be implemented via the PCI Express bus using the chip solution PCAN-Chip PCIe FD. With the license from PEAK-System, the FPGA can be programmed via an image with the corresponding functionality.

In addition to the related documentation, the scope of supply includes licenses for the Windows and Linux device drivers, the CAN monitor PCAN-View, and the PCAN-Basic API.

Specifications

- ☐ FPGA implementation of the CAN FD controller
- ☐ Complies with CAN specifications 2.0 A/B and FD
- ☐ CAN FD support for ISO and Non-ISO standards switchable
- ☐ CAN bus connection for PCI Express x1 with bit rates up to 2.5 Gbit/s
- ☐ PCIe data transfer via bus master DMA
- ☐ DMA memory access operations with 32- and 64-bit addresses
- ☐ 100-percent compatible with the PCI Express device drivers and the software from PEAK-System
- ☐ Measurement of bus load including error frames and overload frames on the physical bus
- ☐ Induced error generation for incoming and outgoing CAN messages

Ordering information

Designation	Part No.
PCAN-Chip PCIe FD 2 Channel	IPES-004092
PCAN-Chip PCIe FD 4 Channel	IPES-004093

Scope of supply

- ☐ One license for:
 - Configuration image for FPGA programming depending on the version:
 - PCI Express 2 Channel
 - PCI Express 4 Channel (Quad)
 - Device drivers for Windows 11 (x64), 10 (x64), and Linux
 - CAN monitor PCAN-View for Windows (details on page 94)
 - Programming interface PCAN-Basic for developing applications with CAN connection (details on page 80)
 - Programming interfaces for standardized protocols from the automotive sector
- ☐ Documentation for wiring and integration including a sample circuit diagram

The license fees for CAN FD to Robert Bosch GmbH are included. If you are interested and for more information, please contact info.peak@hms-networks.com.